MODEL: UJ32-C-V-G-FMT-1-P24 | DESCRIPTION: USB RECEPTACLE

FEATURES

- USB Type C receptacle
- USB 3.2 Gen 1
- 5 Gbit/s data transfer speed
- reflow solder compatible
- tube packaging

RoHS

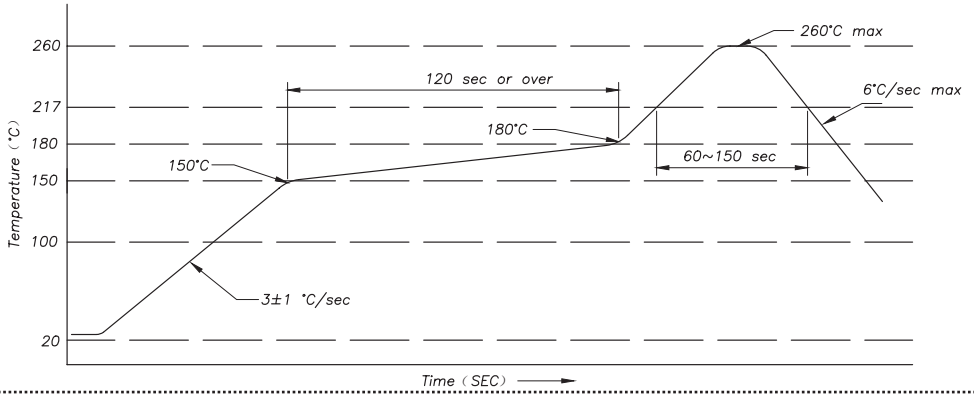
SPECIFICATIONS

parameter	conditions/description	min	typ	max	units
USB standard	USB 3.2 Gen 1				
rated input voltage				48	Vdc
rated input current	as per EIA-364-70, Method B power pins collectively (A1, A4, A9, A12, B1, B4, B9, B12) VCONN pins individually (A5, B5) all other signal pins individually			 5.0 1.25 0.25	 A A A
contact resistance[1]	between terminals and mating plug			40	mΩ
insulation resistance	at 100 Vdc between adjacent contacts	100			MΩ
voltage withstand	between adjacent contacts for 1 minute	100			Vac
insertion force	at a rate of 30 cycles/min	5		20	N
withdrawal force	at a rate of 30 cycles/min	8		20	N
life	at a rate of 500 cycles per hour		10,000		cycles
operating temperature		-25		85	°C
flammability rating	UL94V-0				
RoHS	yes				

Note: 1. When measured at 20 mV / 100 mA.

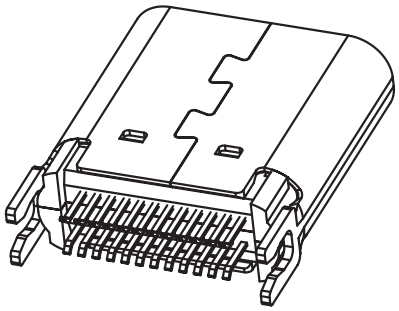
SOLDERABILITY

parameter	conditions/description	min	typ	max	units
reel storage	at 20~30°C, 40~75% humidity				
reflow soldering	see reflow profile			260	°C

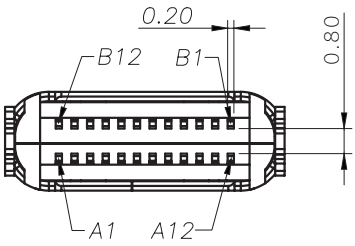


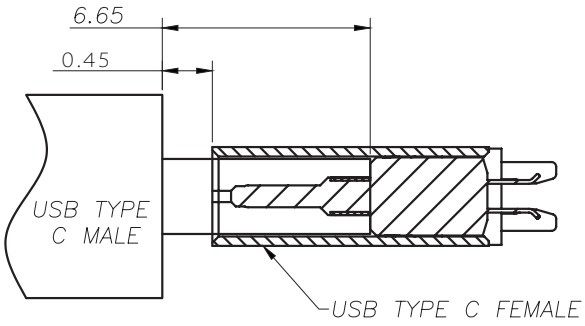
MECHANICAL DRAWING

units: mm
tolerance:
X.X ±0.30 mm
X.XX ±0.20 mm
X.XXX ±0.10 mm
unless otherwise specified

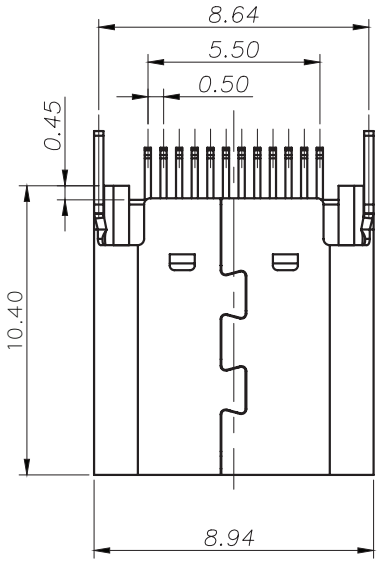


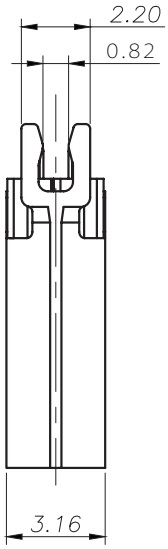
DESCRIPTION	MATERIAL	PLATING/COLOR
housing	PA9T (UL94V-0)	black
contact	C1815	contact area: 3 µ" gold over 50 µ" nickel solder area: 3 µ" gold over 50 µ" nickel
mid plate	SUS301	clear
front shell	SUS304	30 µ" nickel
EMI plate	SUS304	clear

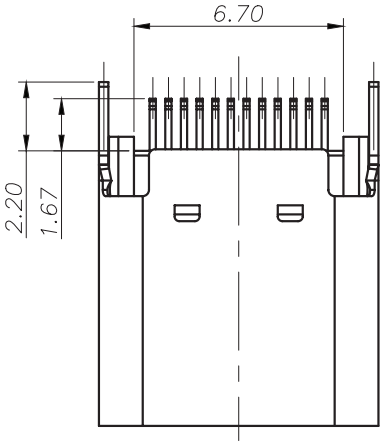


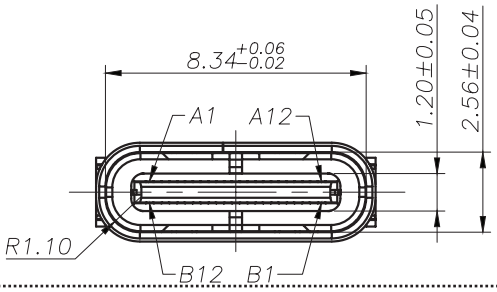


AFTER ASSEMBLY





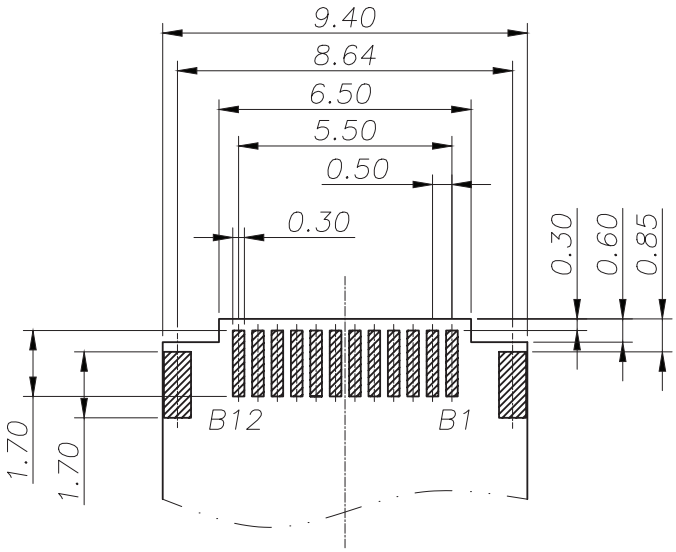




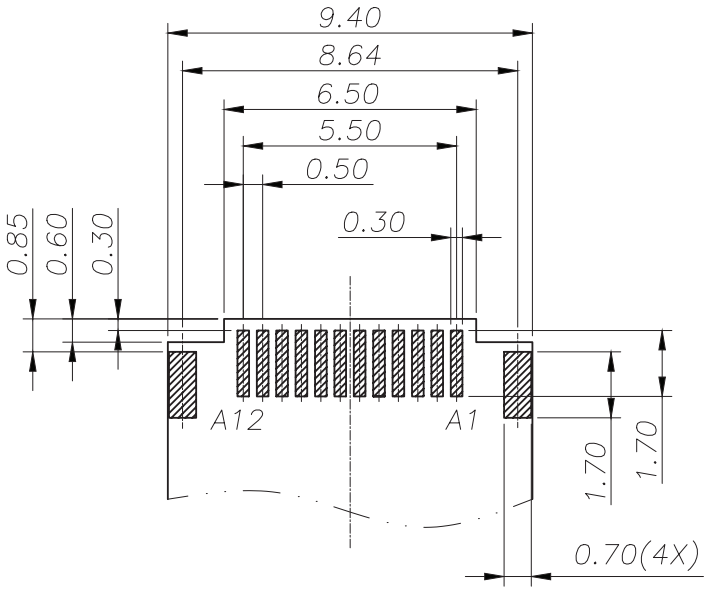
MECHANICAL DRAWING (CONTINUED)

units: mm
PCB tolerance: ±0.05 mm
PCB thickness: 0.80 ± 0.05 mm

PIN CONNECTIONS			
PIN	SIGNAL NAME	PIN	SIGNAL NAME
A1	GND	B12	GND
A2	TXp1	B11	RXp1
A3	TXn1	B10	RXn1
A4	VBUS	B9	VBUS
A5	CC1	B8	SBU2
A6	D+	B7	D-
A7	D-	B6	D+
A8	SBU1	B5	CC2
A9	VBUS	B4	VBUS
A10	RXn2	B3	TXn2
A11	RXp2	B2	TXp2
A12	GND	B1	GND



Recommended PCB Layout
Bottom View



Recommended PCB Layout
Top View

REVISION HISTORY

rev.	description	date
1.0	initial release	08/01/2024

The revision history provided is for informational purposes only and is believed to be accurate.